

Notice of References Cited	Application/Control No. 10/591,178	Applicant(s)/Patent Under Reexamination BLANCHARD, PIERRE	
	Examiner Peniel M. Gumedzoe	Art Unit 2891	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-3,719,535 A	03-1973	Zoroglu, Demir S.	438/549
*	B	US-4,772,565 A	09-1988	Kamimura et al.	438/59
*	C	US-5,192,990 A	03-1993	Stevens, Eric G.	257/229
*	D	US-5,343,064 A	08-1994	Spangler et al.	257/350
*	E	US-6,380,005 B1	04-2002	Nakashiba, Yasutaka	438/144
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	S. Wolf, Silicon Processing for the VLSI Era, Vol. 3, 1995, Lattice Press, Page 331
	V	
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.